



INTERRUPT REQUEST CONTROLLER
Kendell A.Chilton
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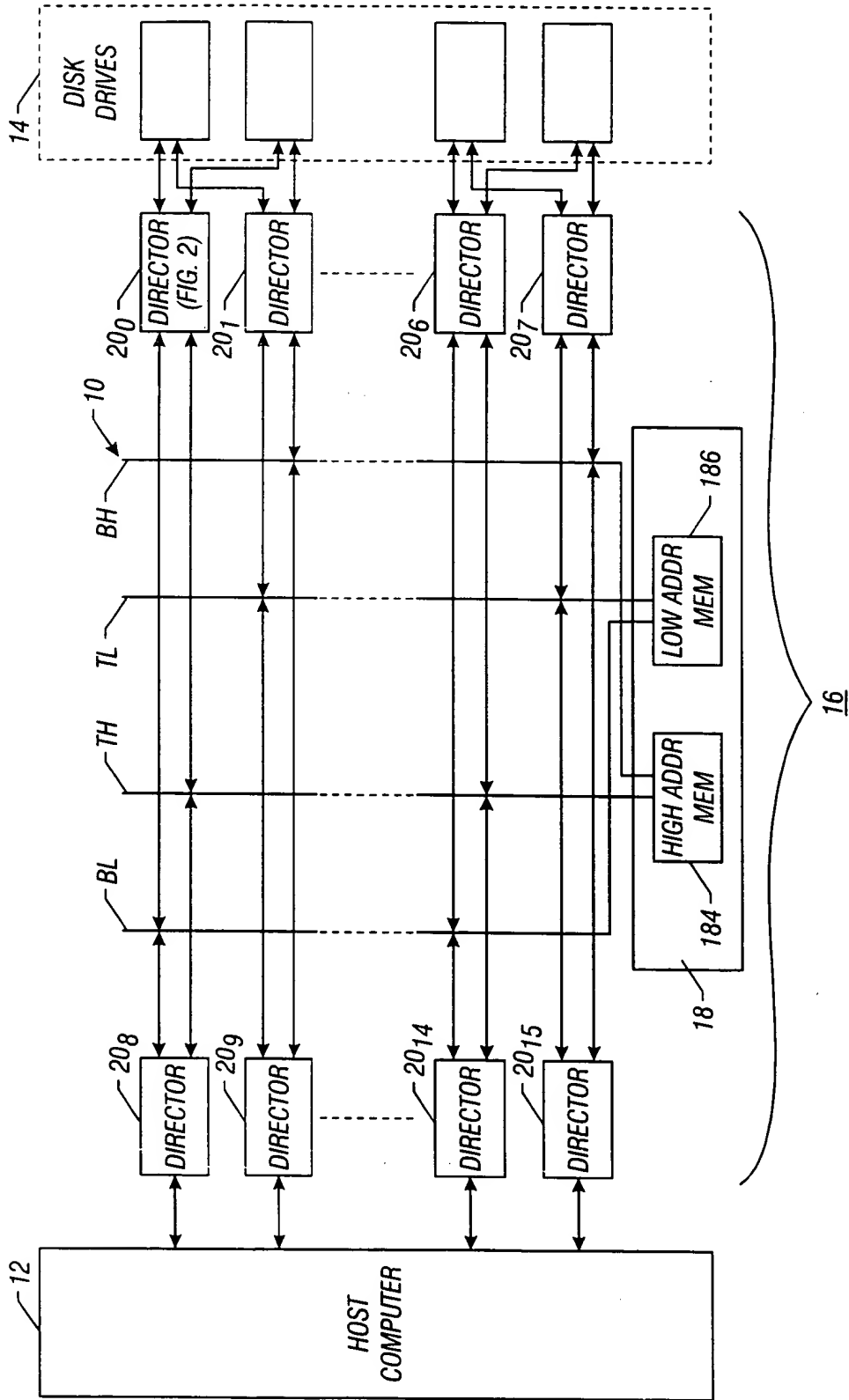


FIG. 1



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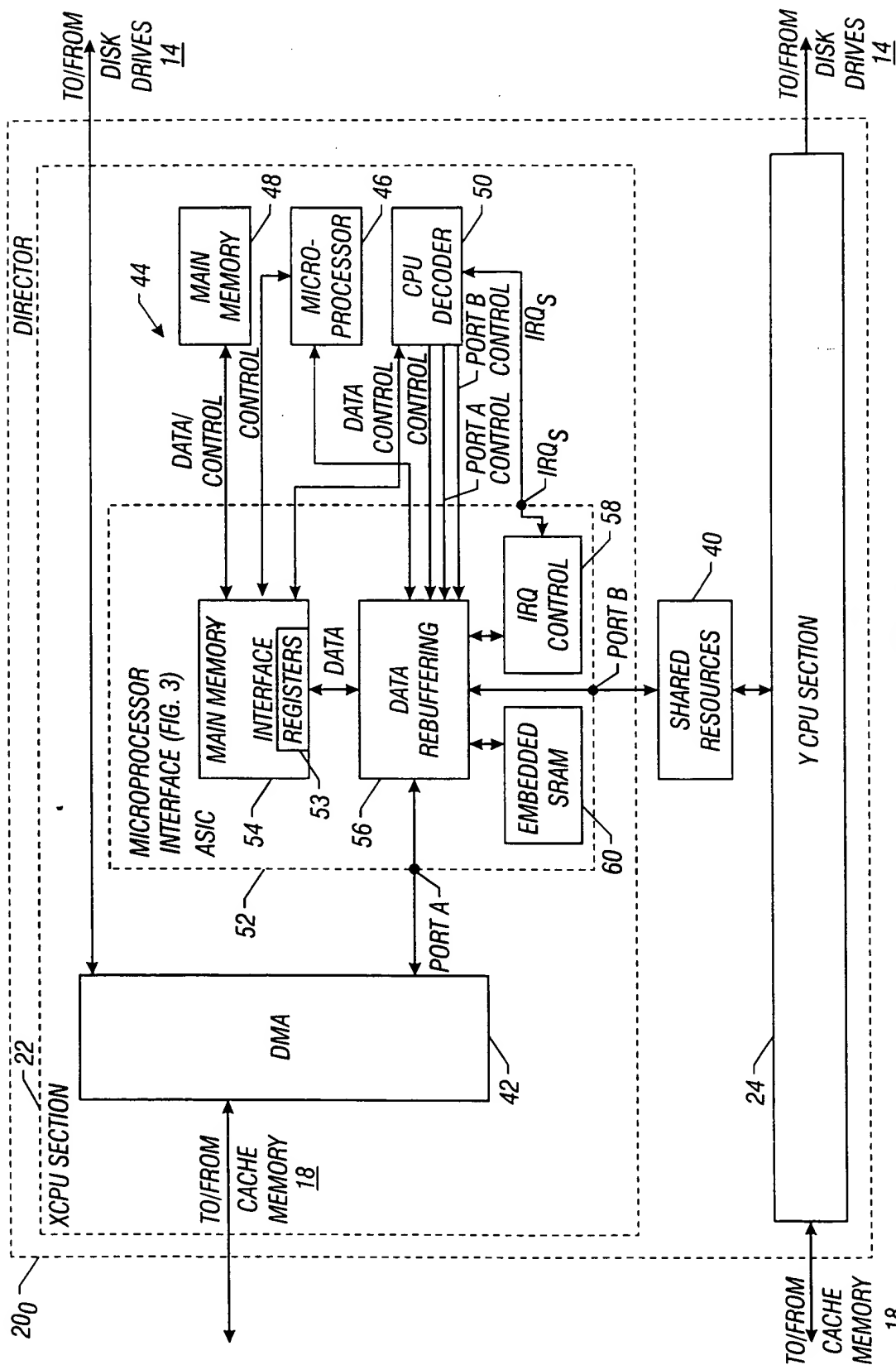


FIG. 2



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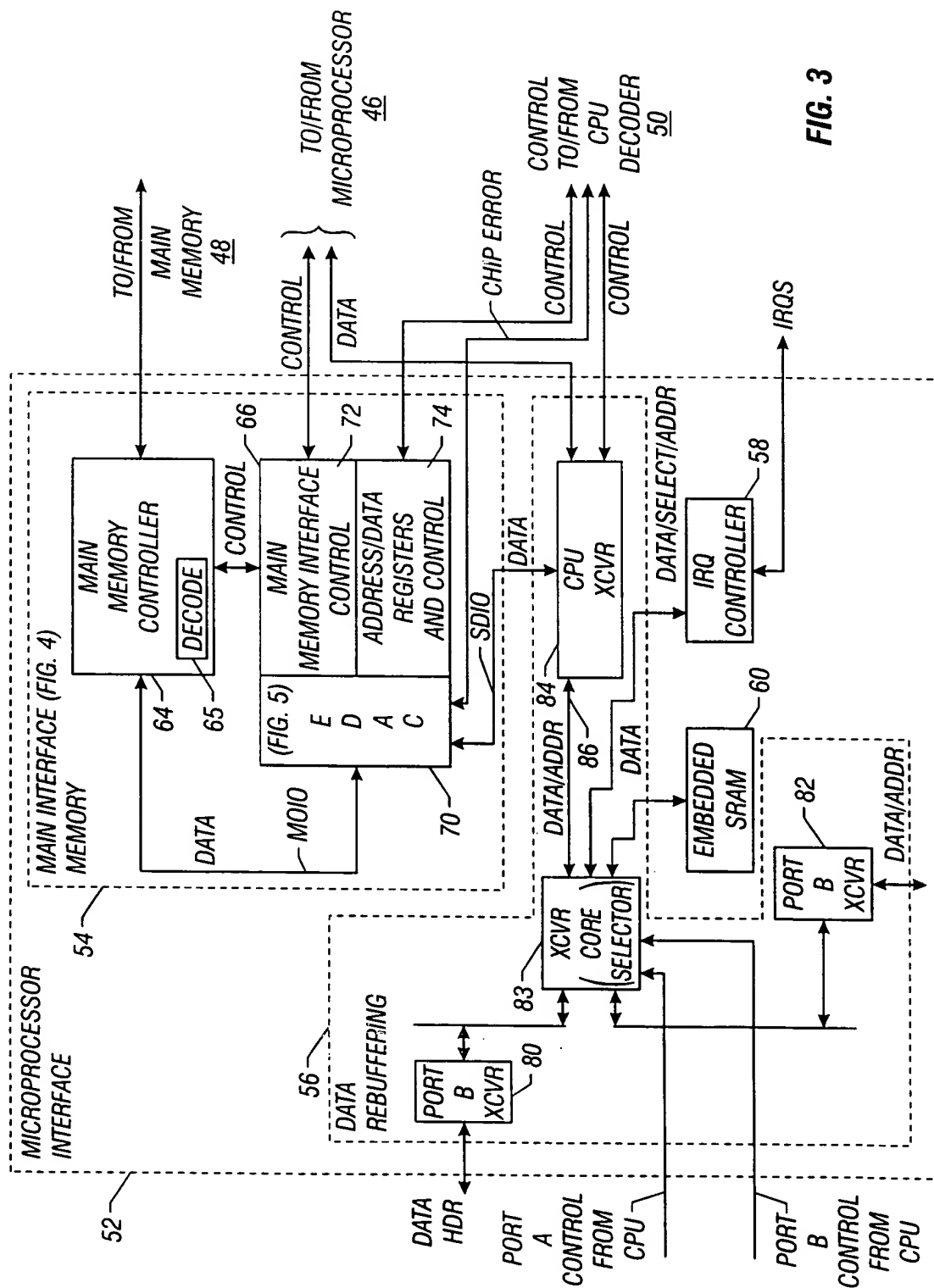


FIG. 3



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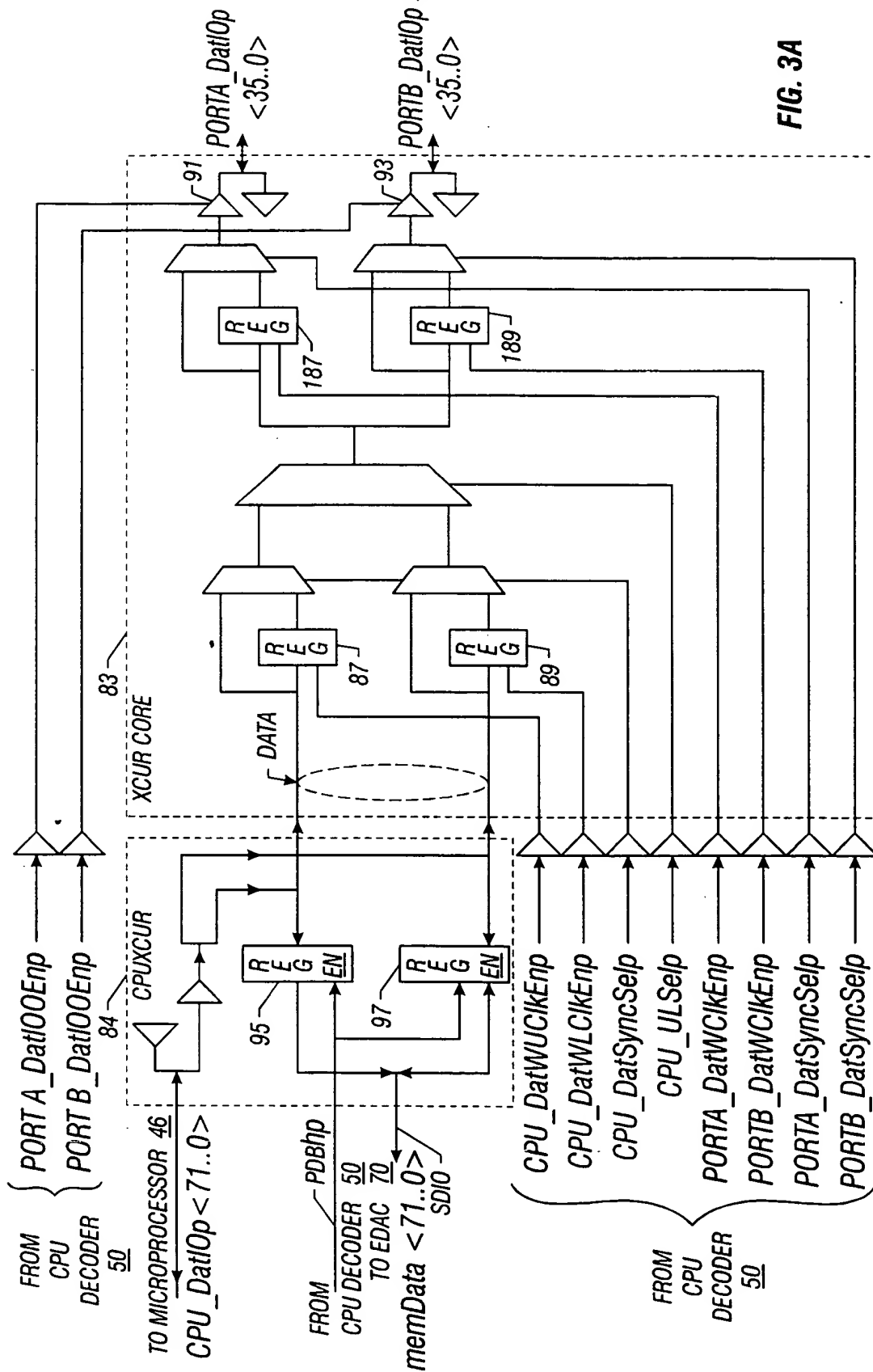
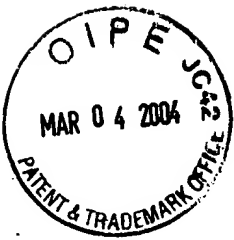


FIG. 3A

[illegible]



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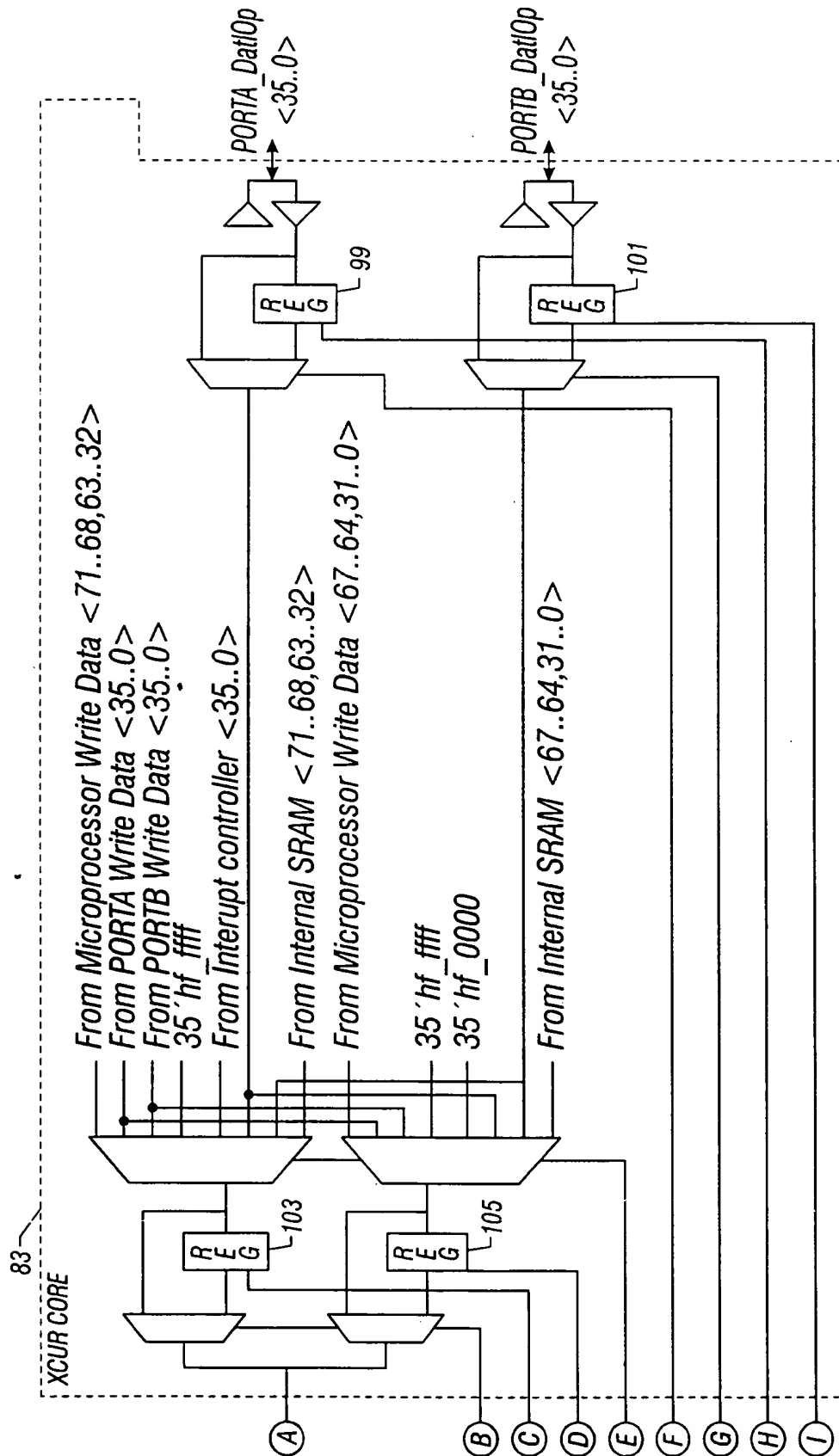


FIG. 3B-2



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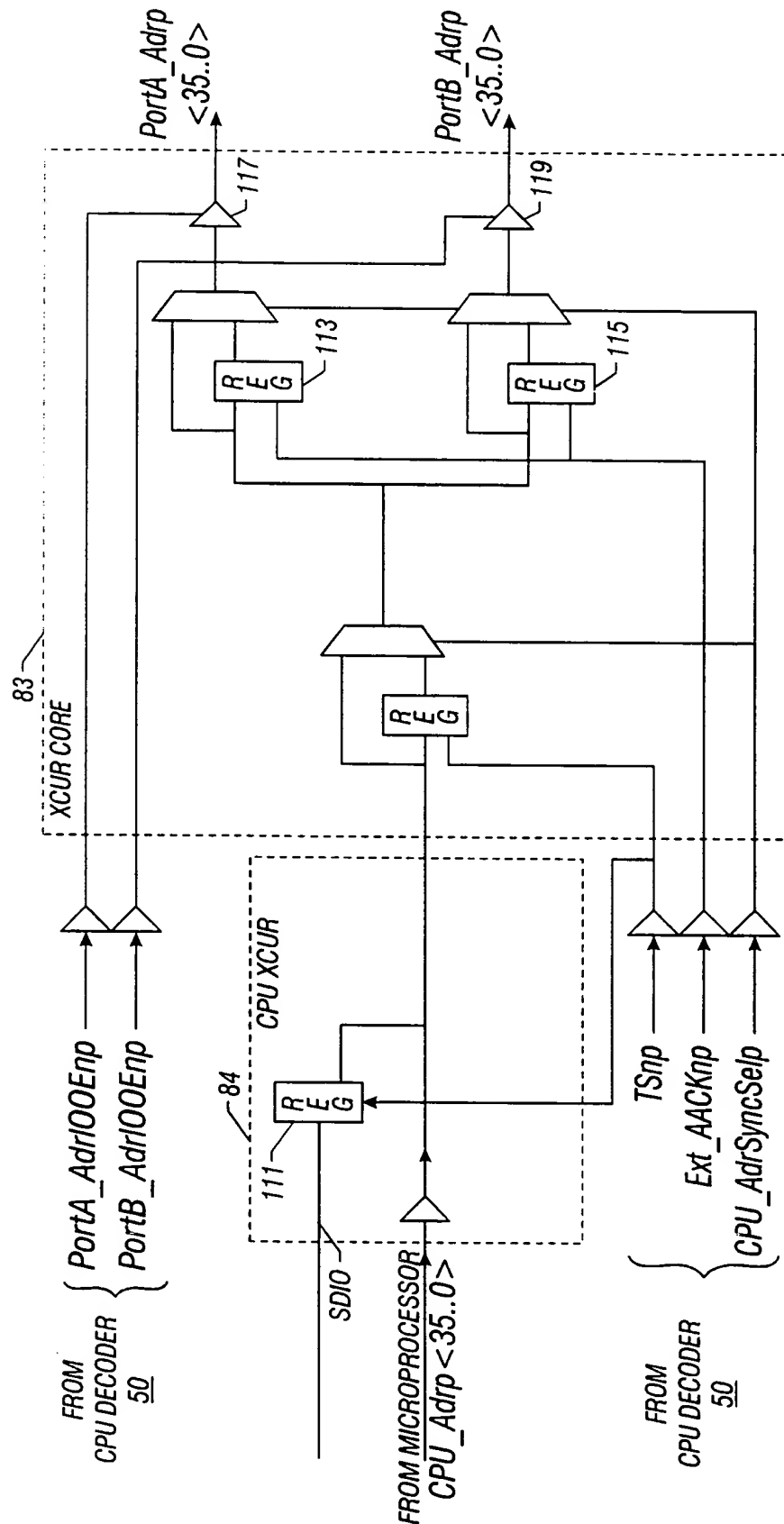
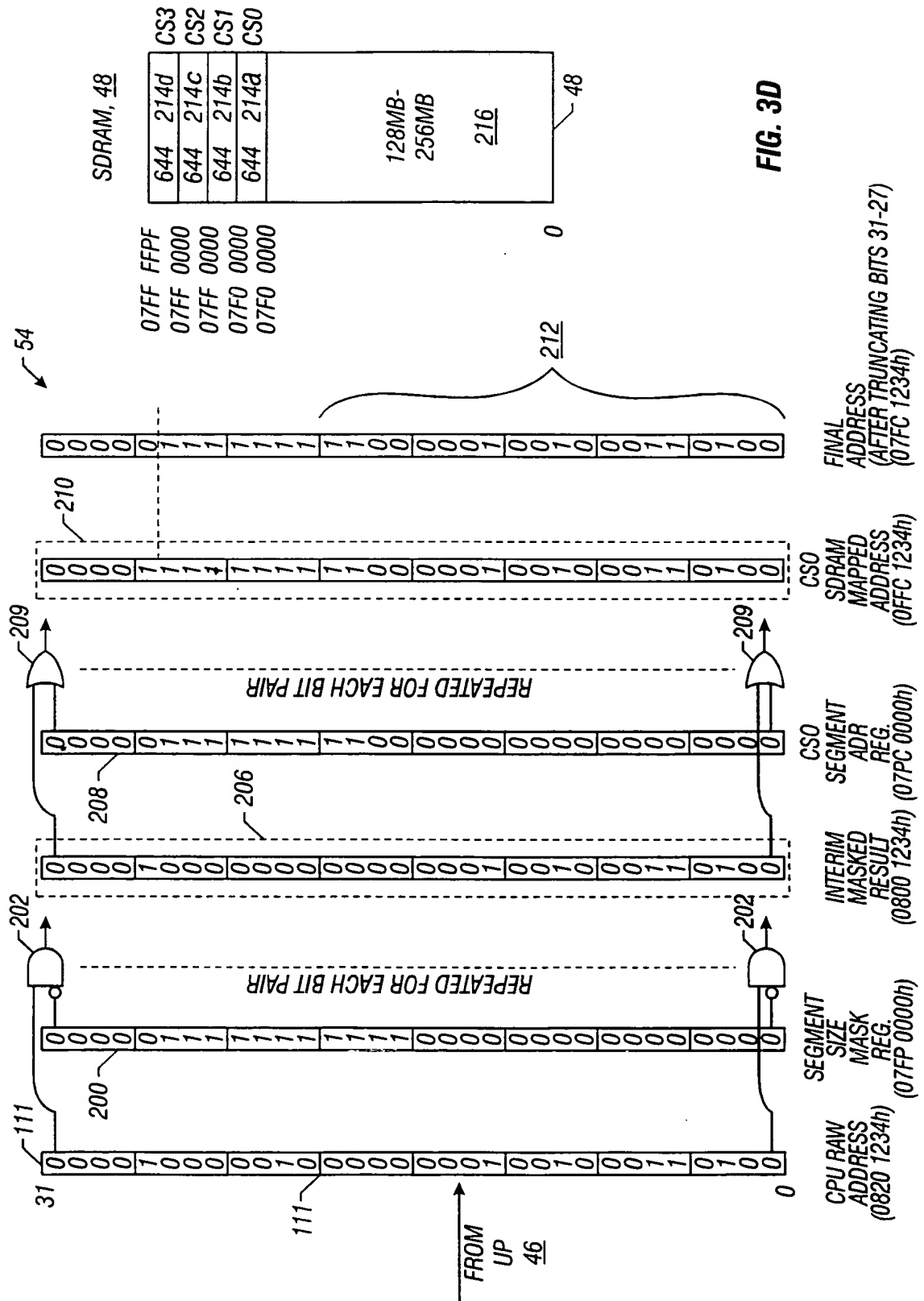


FIG. 3C



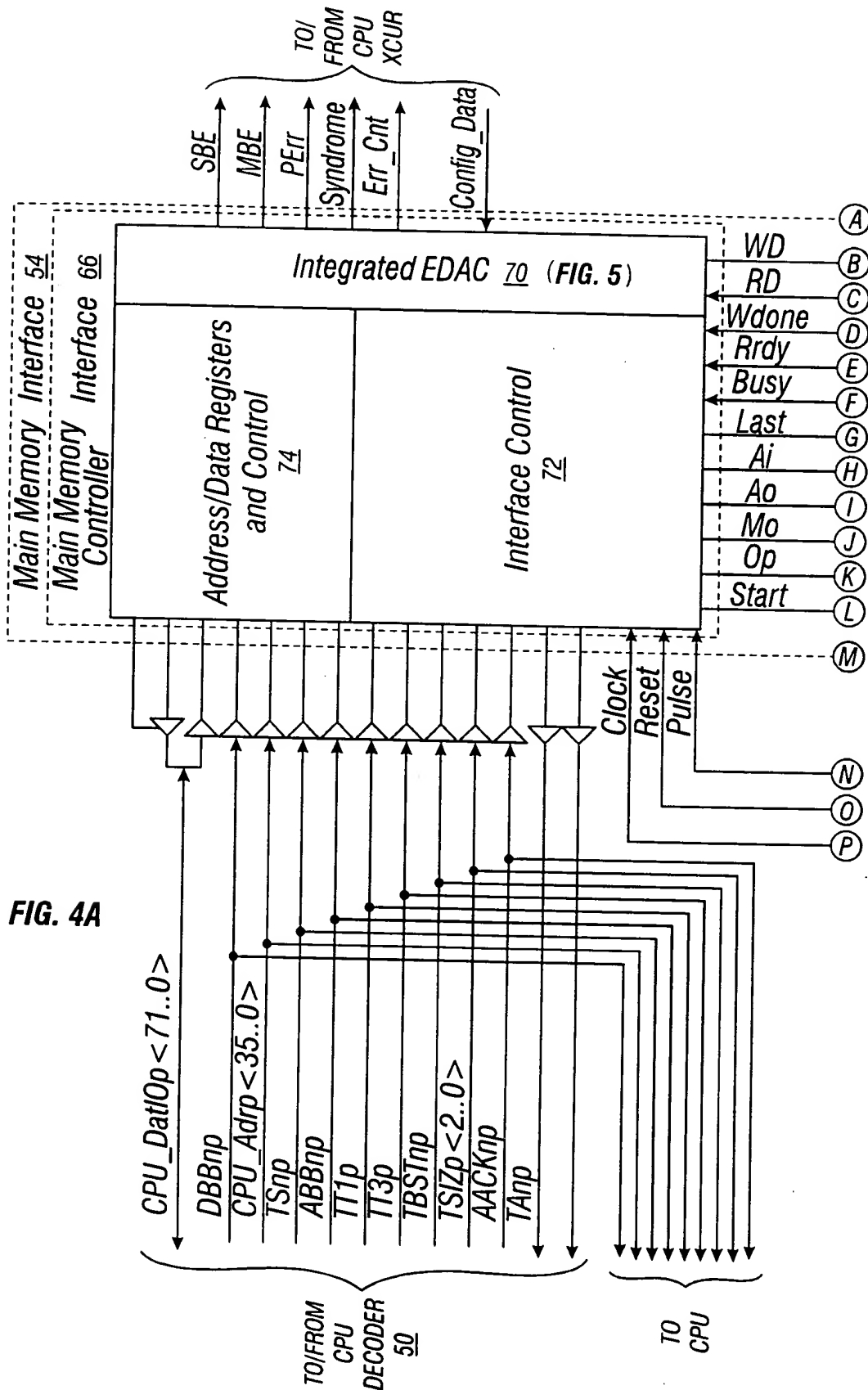


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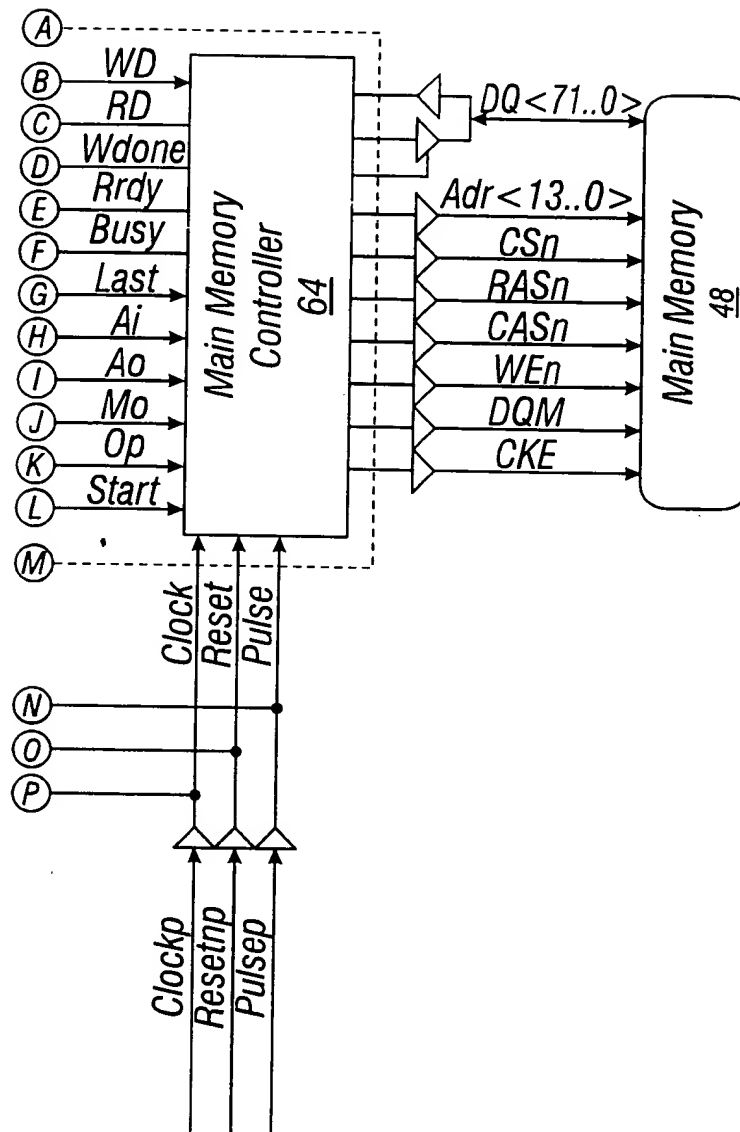


FIG. 4B

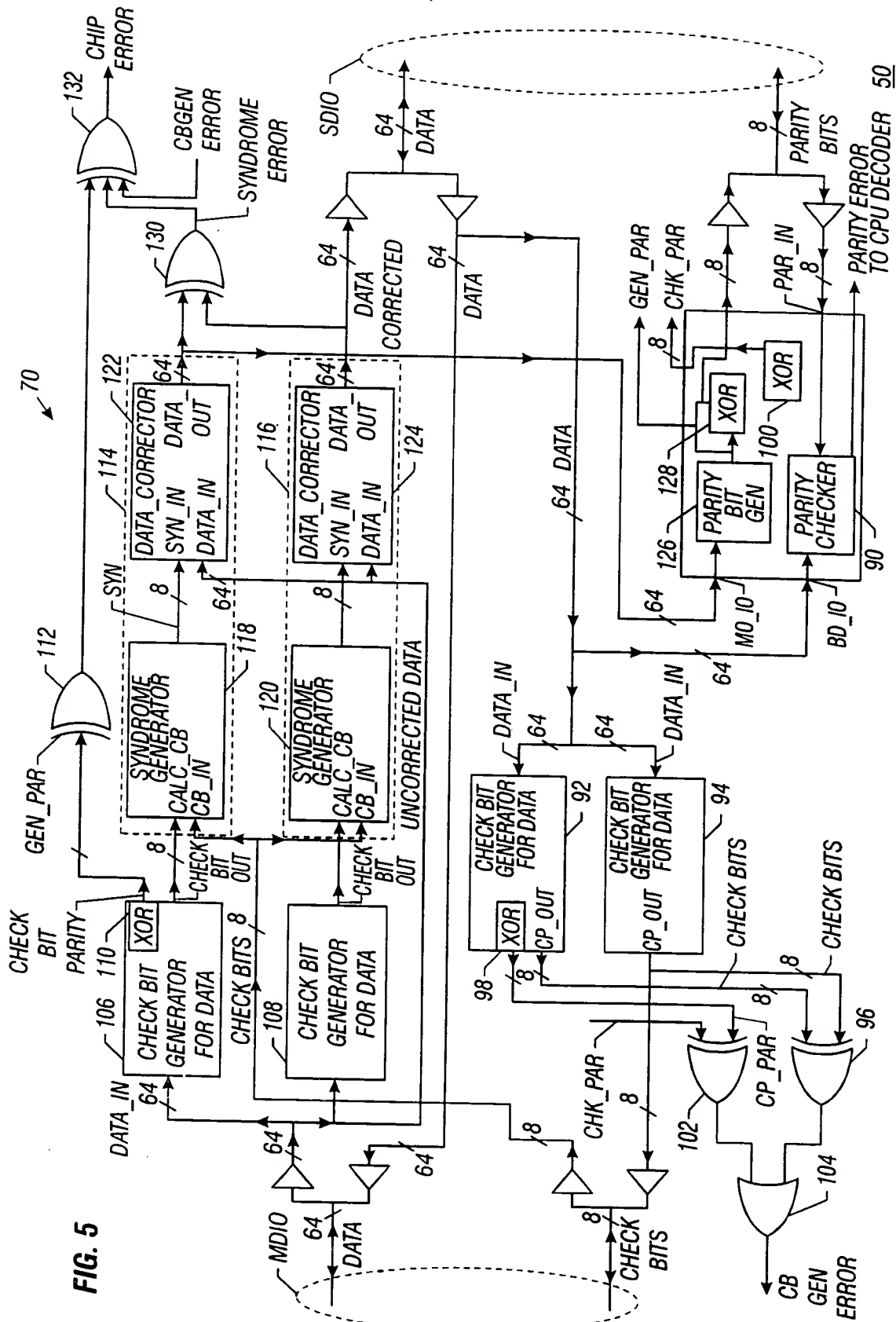


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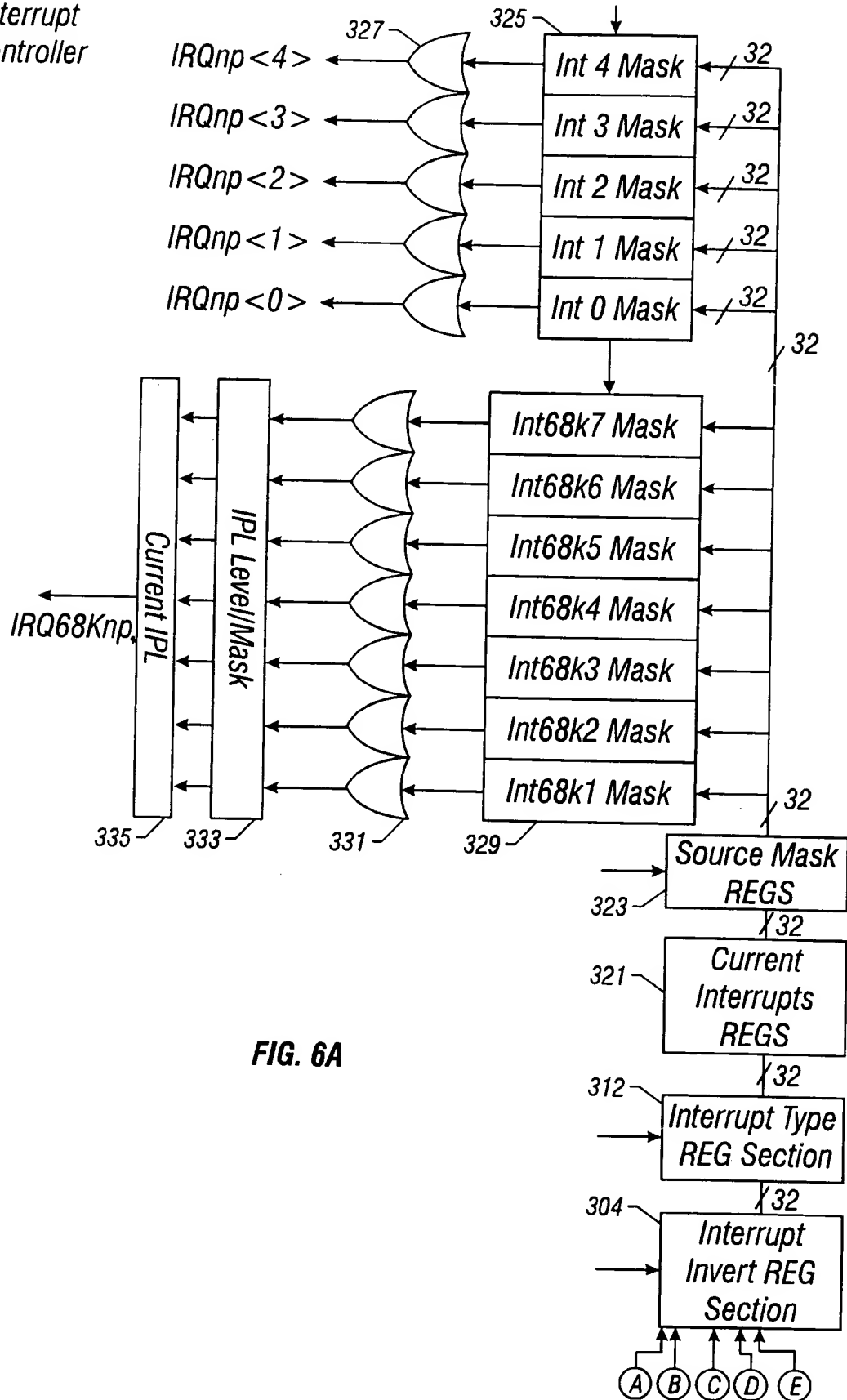


FIG. 6A



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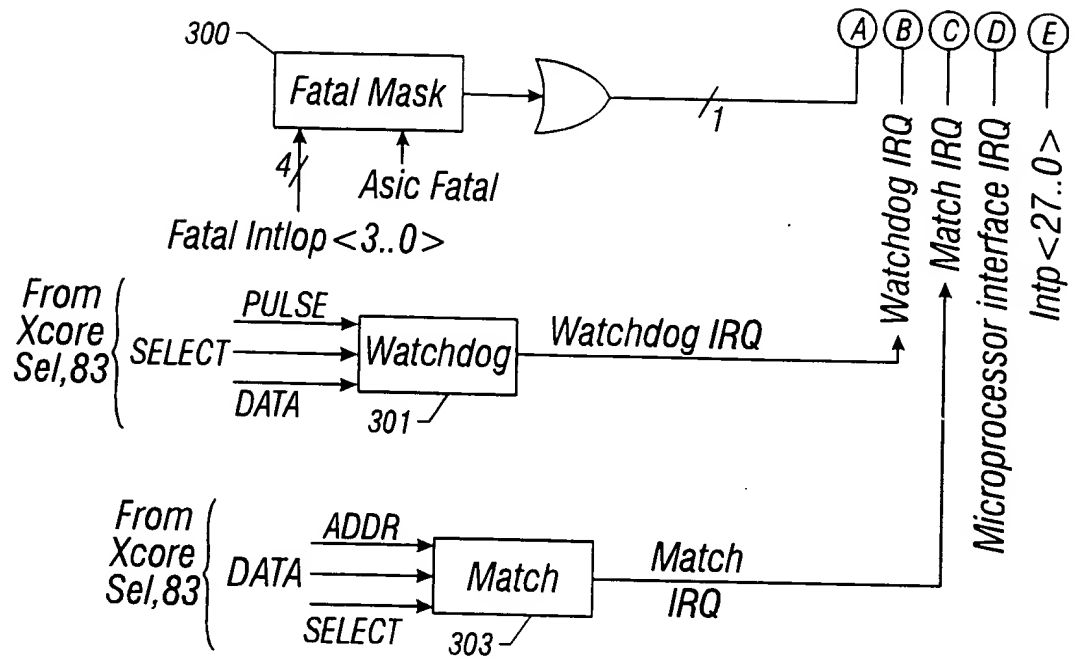


FIG. 6B



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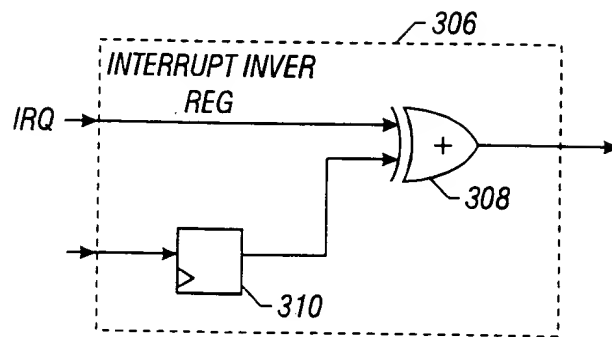


FIG. 7

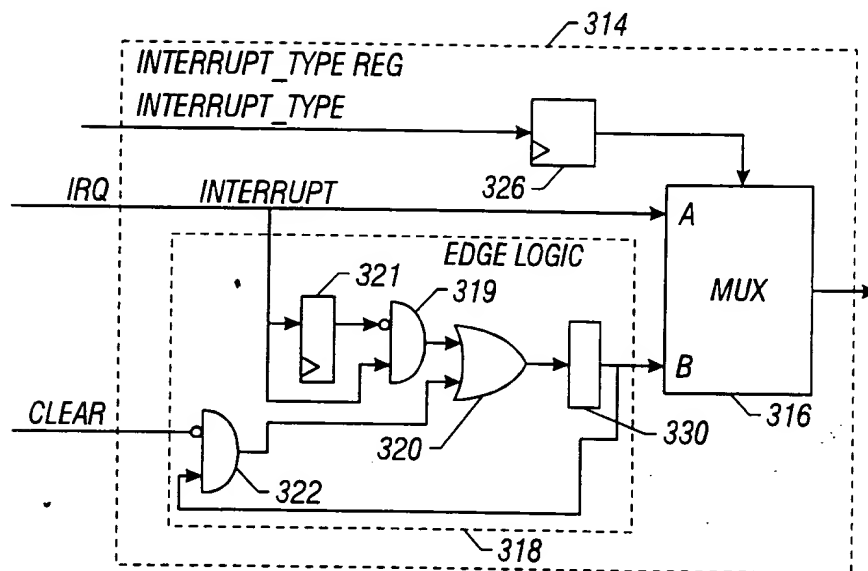


FIG. 8

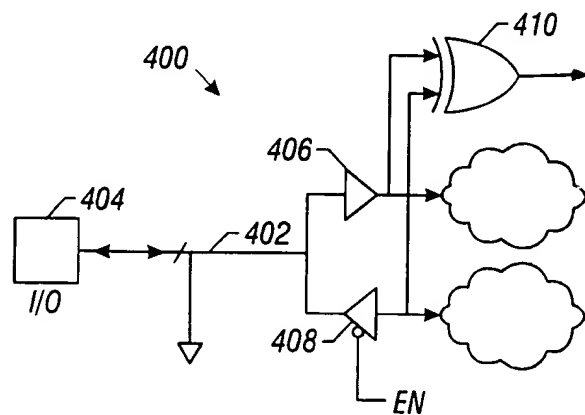


FIG. 9